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FIG. 1

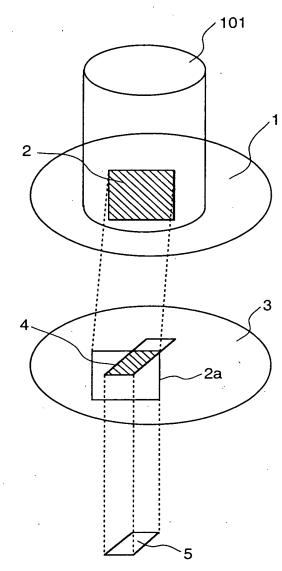


FIG. 2

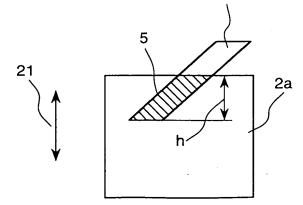


FIG. 3(a)

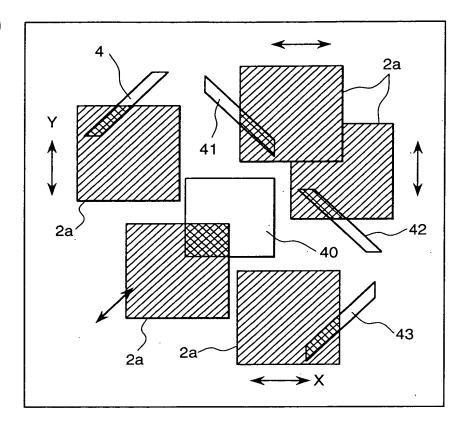


FIG. 3(b)

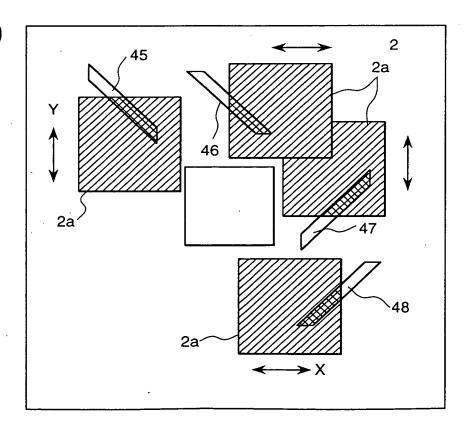
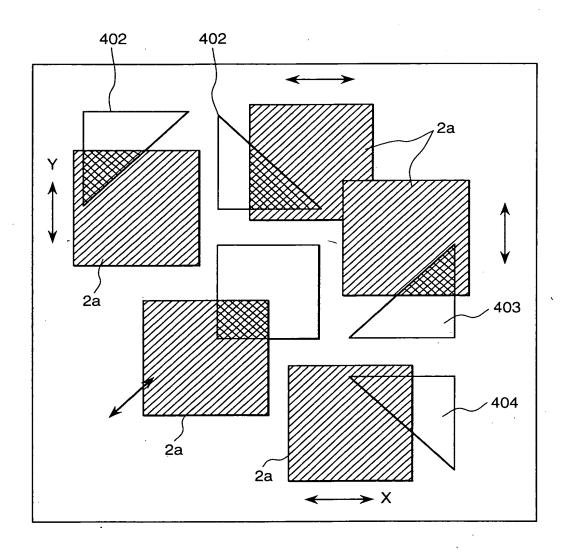


FIG. 4

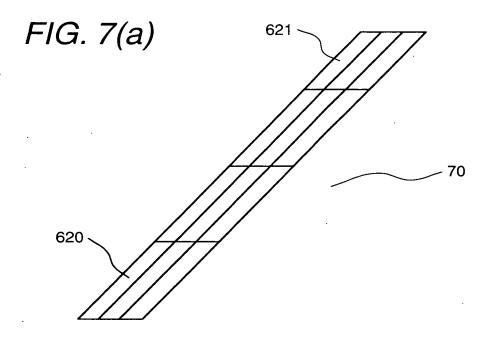


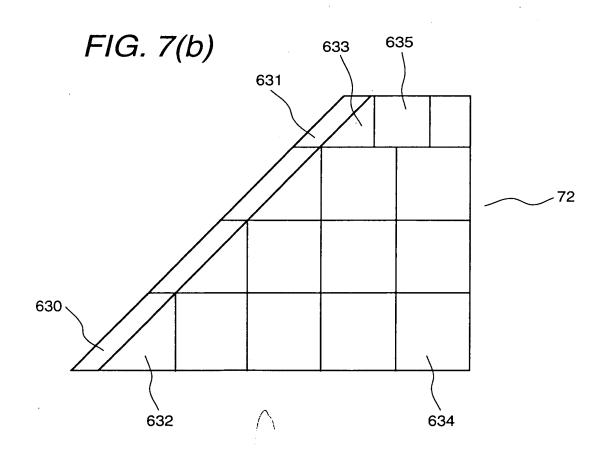
× × DEFLECTION CONTROL CIRCUIT APERTURE CONTROL CIRCUIT STAGE CONTROL APERTURE-NUMBER GENERATION DRAWING CONTROL CIRCUIT DRAWING-PATTERN-DATA GENERATION CIRCUIT CONTROLLER

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F/G. 6

	FIGURE	FIGURE CODE	<u>.</u> *	FIGURE	FIGURE CODE
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PARALLELOGRAM 1		B-1	TRIANGLE 1		
PARALLELOGRAM 2		B-2	TRIANGLE 2		C-2
PARALLELOGRAM 3		B-3	TRIANGLE 3		
PARALLELOGRAM 4		B-4	TRIANGLE 4		Q-4-0





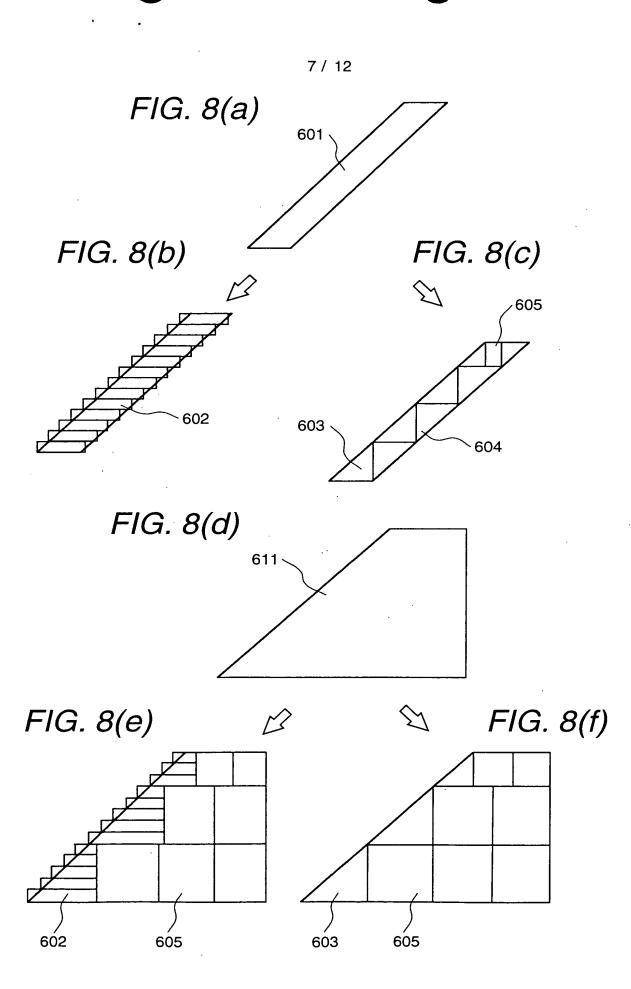
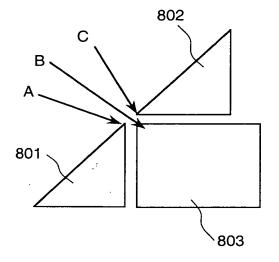
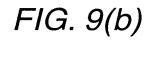


FIG. 9(a)





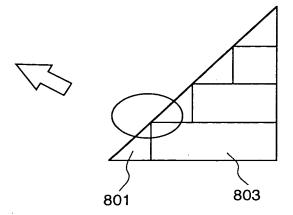


FIG. 9(c)

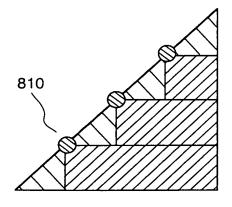
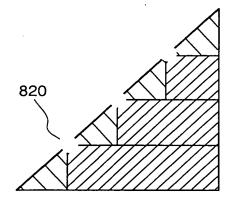
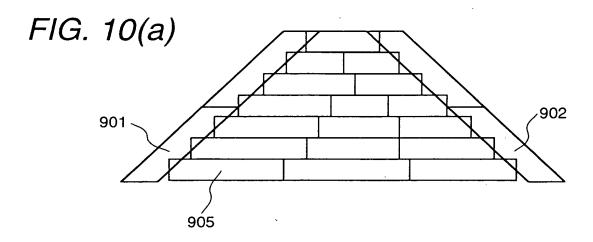
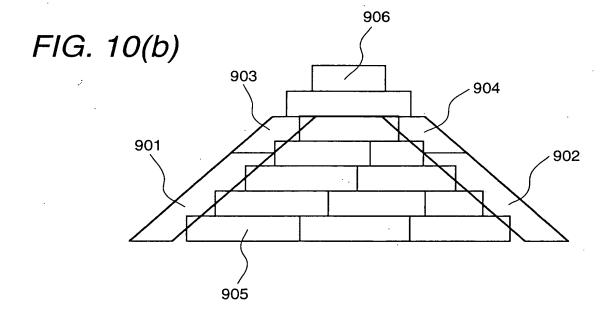


FIG. 9(d)







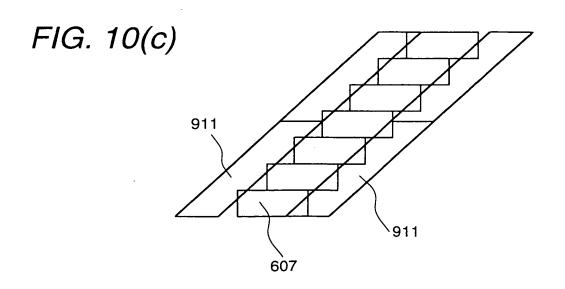


FIG. 11

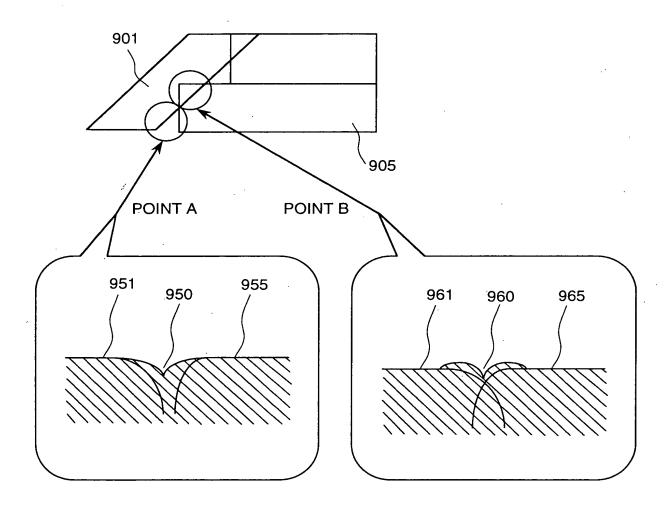


FIG. 12

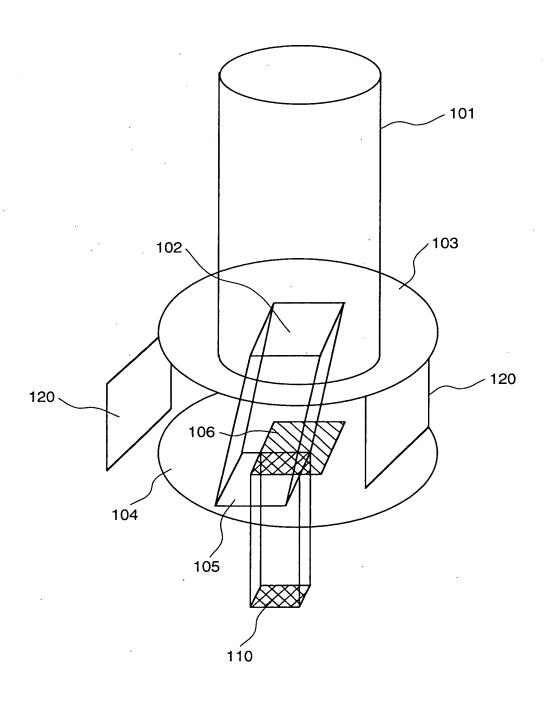


FIG. 13(a)

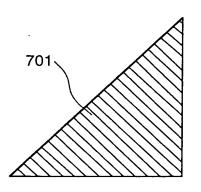


FIG. 13(b)

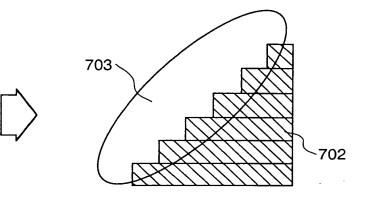


FIG. 13(c)

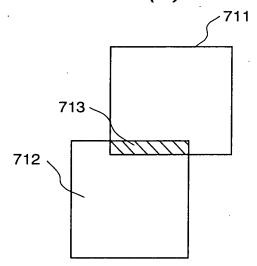


FIG. 13(d)

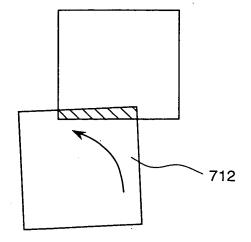


FIG. 13(e)

